

Notice of References Cited	Application/Control No. 10/646,179	Applicant(s)/Patent Under Reexamination CHAO ET AL.	
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
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	I	US-			
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FOREIGN PATENT DOCUMENTS

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	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Connors, D.P. and Yao, D.D. "Methods for Job Configuration in Semiconductor Manufacturing", IEEE Transactions on Semiconductor Manufacturing, Aug. 1996, vol. 9, No. 3, pgs. 411.
	V	Feigin, G.E.; Katircioglu, K.; Yao, D.D., "Capacity allocation in semiconductor fabrication," Decision and Control, 1999. Proceedings of the 38th IEEE Conference on , vol.2, no., pp.1374-1379.
	W	Hood, S.J.; Berman, S.; Barahona, F., "Capacity planning under demand uncertainty for semiconductor manufacturing," IEEE Transactions on Semiconductor Manufacturing, May 2003, vol.16, no.2, pp. 273-280.
	X	Cakanyildirim, M. and Roundy, R. SeDFAM: Semiconductor demand forecast accuracy model, IIE Trans 34(5) (2002), 449-465.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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